What is claimed is:

1. An integrated PCI interface card, comprising:

at least two bus masters;

a control unit for generating bus request and bus acknowledge signals of the two bus masters; and

a multiplexer for selecting an unused address to act as an identification selection signal of one of the at least two bus masters.

2. The integrated PCI interface card of Claim 1, wherein the at least two bus masters share a same interrupt control signal.

3. The integrated PCI interface card of Claim 1, further comprising an EEPROM for assigning an initial identification selection signal to one of the at least two bus masters at power start-up and recording the unused address after power start-up.

4. An integrated PCI bus system, comprising:

at least one integrated PCI interface card as recited in Claim 1;

a PCI host controller for arbitrating which PCI interface card owns use permission;

at least one bus request signal issued by the at least one integrated PCI interface card to the PCI host controller;

at least one bus acknowledge signal issued by the PCI host controller to the at least one integrated PCI interface card for replying the at least one bus request signal; and

at least one identification selection signal issued by the PCI host controller for selecting one bus master of the integrated PCI interface card.

5. The integrated PCI bus system of Claim 4, further comprising

at least one PCI interface card having a single bus master.

6. The integrated PCI bus system of Claim 4, wherein the at least two bus masters of the integrated PCI interface card share a same interrupt control signal.

7. The integrated PCI bus system of Claim 4, wherein the PCI host controller assigns an unused address as another identification selection signal of the integrated PCI interface card by means of using a software program to scan unused address lines after power start-up.

8. The integrated PCI bus system of Claim 4, wherein the at least two bus masters of the integrated PCI interface card own a use permission in a rotating turn.